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Dma Coprocessor

MIPs CPU, Mixed Signal & Consumer
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ASTRA - Advanced Sysinfo Tool - Example of the Report

... **Processor:** AMD Athlon XP 1800+ **Coprocessor:** Internal CPU ...
Device: 0001h LSI53C810 PCI to SCSI I/O **Processor Class:** 01h ... X]
I/O Access: [X] Bus **Master:** [X] Device ...
www.sysinfofab.com/astra.htm - 60k - [Cached](#) - [Similar pages](#)

The Echelon Lighting Control System

... Others add a **coprocessor** -- such as the 8X305 or TMS320 ... The Executive **Processor**
uses multitasking ... A second task is the P4 bus **master**, controlling all transfers ...
www.zetetics.com/bj/papers/echpaper.htm - 27k - [Cached](#) - [Similar pages](#)

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... & hierarchical architecture to **master** complexity and ... bus ... DSP RAM **processor** bus
Communication ... abstract interface to communication **coprocessor**/controller API ...
tima.imag.fr/MPSOC/2002/slides/MPSOC_02aaj.pdf - [Similar pages](#)

EP patents matching keyword 'bus'

... EP347929, Parallel processor. EP426184, Bus **master** command protocol. ... EP267613,
Micro **processor** capable of being connected with **coprocessor**. ...
gauss.bacon.su.se/indices/keyword/19/ - 101k - [Cached](#) - [Similar pages](#)

Amiga Nostalgia

... Flow, New Horizons, Ideas **Processor**, \$99.95. ... Interactive Movie" game developed by
Master Designer/Mindscape, ... Math **Coprocessor** Board, Netch Computer Products,
68881 & ...
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... processors where the Arbiter performs classic **DMA** functionality. ... Since the TCU is a
co-processor, it is ... A default **master processor** executes this so-called boot ...
www.ics.ele.tue.nl/~epicurus/publications/jsa03.pdf - [Similar pages](#)

TechiWarehouse - A+ Questions and Answers

... A: **Master**. 19: What factor determines the amount of RAM a CPU can control? ... 258:
What type of **processor** has built in math **coprocessor** and cache? ...
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... Combining the **coprocessor** and general arithmetic instructions yields ... allows the GPP
to be the system **master**. ... modem and a dedicated applications **processor** on a ...
focus.ti.com/pdfs/vf/wireless/multimedia.pdf - [Similar pages](#)

Astra SysInfo tool v4.10

... crystal (except NexGen Nx586/686); **coprocessor** is identified ... The number is 1 for

single **processor** systems without ... Access, I/O Access, Bus **Master**: Possible ways ...
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ROM BIOS and the first MEG of memory

... 001C, 4, Interrupt Vector for INT 07h (**processor** extension not ... Vector for INT 75h
(IRQ13 math **coprocessor** exception). ... 046C, 4, **master** clock count (long integer) 0 ...
www.cybertrails.com/~fys/rombios.htm - 62k - [Cached](#) - [Similar pages](#)

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